



DEPARTMENT OF ELECTRONICS ENGINEERING
डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

क्रमांक: DoECE / Ph.D. Adm/ ११८ / 2025-26

दिनांक: 25/07/2025

Ph. D. Admission (Special Round)

List of Eligible/ Provisionally Eligible Candidates for the DoECE, July - 2025

Academic Year-2025-26

Sr. No.	App. ID	User Name	Candidate Name	Ph.D. Cat.	Eligible / Provisionally Eligible/Not Eligible
1.	12245	SVNIT3PCFL	Tushar Takher	FIR	E
2.	12288	SVNIT3PUGR	Thakre Nilesh Ramesh	ERS(Part time)	PE
3.	12289	SVNIT3PLER	Ankit Kumar	FIR	PE

* Application received via email due to internet issue at the time of submission

Note: E - Eligible, **PE**- Provisionally Eligible, **NE**- Not Eligible

- The candidates must produce original documents with relevant mark-sheets of all semesters of B.E./B. Tech. and M.E./M. Tech. provisional passing certificate/final degree certificate at the time/day of the interview failing which they will not be allowed to participate in admission process.
- Candidates applied under OBC category must produce **Fresh NCL** in English language.
- Candidates who have applied in FIR category need to produce the GATE Score card.
- All **PE** candidates applied under **ERS(Part Time)** category for Ph.D.Admission must enclose self-attested copies of the following documents/certificates. Same must be produced in original at the time of interview/physical documents verification.
 - a. Appointment letter issued by the current employer.
 - b. Experience certificate duly signed by competent authority on the letter head of the Company/Organization/Institute.
 - c. The external candidates should produce fresh NOC/Sponsorship letter strictly as per format in Appendix.
 - d. All other documents like Date of Birth and Identity Proofs, all semester marksheets, degree certificates.

➤ For any queries contact office@eced.svnit.ac.in

Instruction For Candidates

- The above Candidates are requested to Report at the Department of Electronics Engineering on **31/07/2025 at 10:30 AM** with **all Original Documents**. **Short-listed candidates must remain present for the interview/presentation at Room No. EC004N Conference room, Ground Floor.**
- The Presentation/ interview will **start at 11:30 AM**. Candidates are required to give a presentation as per the following guidelines.
 1. The presentation should not be more than a maximum of 10 minutes duration.
 2. A PowerPoint presentation is a must.
 3. The maximum number of slides including the title slide should not be more than 8.
 4. The presentation should contain the background of topic, literature survey, research gap, problem identification and research methodology.
- Provisionally eligible candidates will be allowed to appear only if they submit appropriate documents at the time of document verification (with a self-attested copy).

[Signature]
25/7/25

Dr. Z. M. Patel / Dr. J. N. Patel
Ph. D In-charges

[Signature]
25/7/2025

Dr. (Ms.) Shilpi Gupta
Head, DoECE